

SOHAIL SHEIKH
1462-Link Drive
Garnet Valley, PA 19060
Work: (610) 499 - 4567

PERSONAL DATA

Date of Birth: June 1, 1953
Marital Status: Married
Children: Two

EDUCATION

Ph. D. Electrical Engineering, Syracuse University, 1989
M. S. Computer Engineering, Syracuse University, 1985
M. S. Electrical Engineering, Syracuse University, 1985
M. Sc Physics, Govt. College Lahore, Pakistan, 1977

TEACHING EXPERIENCE

9/97 - Present Associate Professor, Widener University,
Dept. Of Electrical Engineering.
9/95 - 8/97 Associate Professor, Widener University,
Dept. Of Electrical Engineering and
Computer Science.
9/89 - 8/95 Assistant Professor, Widener University,
Dept. Of Electrical Engineering
1987 - 89 Teaching Assistant, Syracuse University,
Dept. Of Electrical and Computer Engineering
1985 - 87 Research Assistant, Syracuse University,
Dept. Of Electrical and Computer Engineering
1978 - 81 Assistant Professor, Lahore, Pakistan
Govt. College of Science

RESEARCH and CONFERENCE PAPERS

1. S. Baqai, F. Baqai, U.Hamed, S.Sheikh and Ashfaq Khokhar
" Error resilience of EZW coder for Image transmission in Lossy
networks", published in IEEE International Conference on Multimedia
Software Engineering (MSE), Newport Beach, CA, December 2002.
2. Sohail Sheikh, Suk-Chung Yoon, Ahmad Saeed, and E. K. Park
" Multicast Routing Infrastructure in ATM networks ",
Seventh International Conference On Computer Communications And
Networks, sponsored by IEEE and NASA, October 12 - 15, 1998.
3. Sohail Sheikh and Raj Ganesan

" Replication of Multimedia Data Using Master-Slave Architecture ",
The Twenty-First Annual International Computer Software &
Applications Conference ", sponsored by IEEE, August 11 - 15,
Washington, D. C.1997.

4. Sohail Sheikh and Raj Ganesan
" Improvements in Multimedia Data Buffering Using Master/Slave
Architecture ", " The Second World Conference on Integrated Design &
Process Technology ", sponsored by Society for Design and Process
Science, Dec 1 - 4, Austin, Texas 1996.
5. Sohail Sheikh, S. Zafar, and A. Khoker
" Model for Storage and Retrieval of Interactive Video ",
paper was presented in the First International Symposium on
Technologies and Systems for Voice, and Data Communications,
sponsored by SPIE (International Society for Optical Engineering),
held in Philadelphia from 23-26 October 1995.
6. Anil K. Sharma, Sohail Sheikh, Istvan Pelczer, and George C. Levy
" Classification and Clustering: Using Neural Networks ",
Journal of Chemical Information and Computer Sciences,
Volume 34, October 1994, pp.1130 - 1139.
7. Jaehyung Yang, Ashfaq Khokar, Sohail Sheikh, and Arif Ghafoor
" Estimating Execution Time For Parallel Tasks in Heterogeneous
Processing (HP) Environment ",
8th International Parallel Processing Symposium, sponsored by IEEE,
April 26-29, Mexico 1994, pp. 23 - 28.
8. Imran Ghafoor, Ishfaq Ahmad, M. Farrukh Khan, and Sohail Sheikh
" Performance of a Fault-Tolerance Load Balancing Environment for
Parallel Systems ", Minnowbrook Workshop on Parallel and Software
Engineering, July 24 - 27, 1992.
9. P. Sole, A. Ghafoor, and Sohail Sheikh
" The Covering Radius of Hadamard Codes in Odd Graphs ",
appeared in Discrete Applied Mathematics on July 1992, pp. 501 - 510.
10. A. Ghafoor, A. Goel, T. Mingchen, and S. Sheikh
" Reliability Analysis Of A Fault-Tolerant Multi-bus Multiprocessor
System ", IEEE - ACM Third International Symposium on Parallel and
Distributed Processing, December, 1991, Dallas, TX, pp. 436 - 443.
11. A. Ghafoor, S. Sheikh, and Patrick Sole
" Bipartite Distance Regular Interconnection Topology for

Fault-Tolerant Multiprocessor Systems ",
IEE Proceedings, Vol. 137, pt. E. No. 3, May 1990, pp. 173 - 184.

12. A. Ghafoor, M. Guizani, and S. Sheikh
" Architecture of an All-Optical Circuit-Switched Multistage Interconnection Network ", IEEE Journal on Selected Areas in Communications, Vol. 8, No. 8. Oct 1990, pp. 1595 - 1607.
13. A. Ghafoor, I. Ahmad, and S. Sheikh
" Dynamic Task Scheduling Algorithm for Distributed Systems ",
13th Minnowbrook Workshop on Software Engineering,
July 24 - 27 1990.
14. I. Ghafoor, A. Ghafoor, and S. Sheikh
" Optical Image Processing - The Current State of the Art ",
24th Asilomar Conference on Signals, Systems, and Computers,
Nov 4 - 7, 1990, pp. 553 - 556.
15. A. Ghafoor, S. Sheikh, and P. Sole
" Distance Transitive Graphs for Fault-Tolerant Multiprocessor Systems ",
Proceeding of 18th International Conference on Parallel Processing Systems,
1989, pp. 176 - 179.
16. S. Sheikh, A. Ghafoor, and I. Ghafoor
" Fault - Tolerance and Reliability of Multiprocessing Systems - A Review ",
20th Midwest Symposium on Circuits and Systems, Syracuse, June 1988,
pp. 321 - 324

REFEREE and CHAIR to CONFERENCES

- Chair, two session on First International Symposium on Technologies and Systems for Voice, and Data Communications, sponsored by SPIE (International Society for Optical Engineering), held in Philadelphia from 23-26 October 1995.
- IEEE INFOCOM' 94
- ICPP' 93

Reviewed the following Papers:

- (1) Estimation of Execution times on Heterogeneous Supercomputer Architecture.
- (2) Random Routing of Tasks in Hypercube Architecture.
- (3) Techniques to Enhance Cache Performance Across Parallel Program Sections.

OTHER PARTICIPATION

Attended and participated in the following seminars, and workshops:

1. "ISPCon 2002",
Hosted by 3Com, May 21-23, Baltimore Convention Center
Baltimore, MD.
2. "Wavelet Theory: Principles and Applications",
Hosted by IEEE at Rowan University on June 7 and 14, 2002.
New Jersey.
3. A workshop on "How to Engineer Engineering Education",
sponsored by NSF, held at Bucknell University from July 14-19, 2002.
4. "Conference and Workshop",
sponsored by IEEE, Nov 20, 2002.
Baltimore, MD.
5. "XUP Professor Workshop" participated in 2001.
6. "Microsoft Developers Days" attended one day seminar (2001).
7. "Effecting College Teaching" attended workshop (2000).
8. "Mastering Telecommunications Fundamentals" participated in a seminar (2000).
9. "Team-Oriented, Project-Based Collaborative Learning" attended workshop
(1999).
10. "PLD's (Programmable logic devices)", attended a seminar by XILINX (1997).
11. "In System Programmability and Complex PLD's (Programmable logic
devices)", attended a seminar by XILINX (1996).
12. "The Sixth Symposium on The Frontiers of Massively Parallel
Computation", (1996).
13. "Using Computer Networks", attended a Chautauqua Short Course Program,
sponsored by NSF (1996).
14. "Image Processing and its Impact on Architecture", by Dr. Harold Stone,
Seminar Series (1995).
15. "RISC Architecture in the Classroom for the 21st Century" (1995).

16. "Embedded Systems Design Symposium", by Hewlett Packard (1994).
17. "Digital Design Symposium Seminar ", by Tektronix (1993).
18. "VC 42 Neural Network Applications for the 1990's", by IEEE.
19. "A High Performance Asynchronous Neural Computing Machine", Organized by Philadelphia Section of IEEE.

SUPERVISED MASTER'S THESIS

1. "Algorithmic approach towards cryptography" (2003)
2. "Voice Over IP", (2001)
3. "Dynamic Buffering Strategy for a multistage switch", (1999)
4. "Multicast Routing Infrastructure in ATM networks", (1998)
5. "Classification and Clustering: Using Neural Nets", (1994)
6. "A Direct Octagonal Asymmetric Hierarchical Interconnection Network for Massively Parallel System", (1993)
7. "Design of a Microsequencer Application in Generic Computer Interfacing", (1992)

MEMBER of STUDENT'S COMMITTEE on MASTERS THESIS

1. "Video Coding and Transmission Over Wireless Channel", (2002)
2. "Distance Learning Web Design" (2000)
3. "Simulation of the Window NT and Novel NetWare subnets in the School of Engineering", (1997)
4. "Employer Contact Management System for the Career Advising & Placement Services", (1997)
5. "Development of a Fuzzy Expert System Shell Using the JAVA Language", (1997)
6. "Semantic Query Optimization In Object-Oriented Database", (1996)
7. "Real time controller reconfiguration for faults in a dynamic

- systems using Neural Networks", (1996)
8. "An Online Graduate Student Registration System", (1996)
 9. "Development of Class Library for Graphical Control Objects: An Application in Interactive Trainer System for Lyophilization Process" , (1996)
 10. "Utilizing RPC to exploit parallelism and Fault-Tolerance in Computer Networks", (1995)
 11. "Design and Implementation of a Simulator using Object Oriented Techniques", (1995)
 12. "Graphical Control Objects for Simulation Applications", (1994)
 13. "The Design and Development of an Expert System for Academic Advising ", (1994)
 14. "The Implementation of a Real Time Communication Software", (1992)
 15. "The Design and Development of Petri Net Simulation System", (1991).

EE SEMINAR SERIES

Presented a paper: " *Reliability Analysis of a Fault-Tolerant Multi-Bus Multiprocessor System* ", November 25, 1991.

COURSES TAUGHT

Graduate :

Computer Architecture, Computer Structure, Modern Control.

Undergraduate :

Data Structures and Algorithms I & II, Computer Architecture I & II, Electronics, Logic circuit design, Senior Projects I, II.

HONORS and HONORARY SOCIETIES

1. Listed in *Who's Who Among America's Teachers*:

October 1996 (Fourth Edition)

2. Phi Kappa Phi (National Academic Honor Society).
3. Tau Beta Pi (National Engineering Honor Society).
4. Phi Beta Delta (Honor Society for International Scholars).

MEMBER AFFILIATION

Member of IEEE

CITIZENSHIP

USA